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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/585,275	07/05/2006	2835	1400	H0005430.75230 USA	28	5

**CONFIRMATION NO. 2425**

62993  
BUCHALTER NEMER  
18400 VON KARMAN AVE.  
SUITE 800  
IRVINE, CA 92612

**FILING RECEIPT**



\*OC000000032619786\*

Date Mailed: 10/17/2008

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please submit a written request for a Filing Receipt Correction. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections

### Applicant(s)

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**Power of Attorney:** The patent practitioners associated with Customer Number 62993

### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/US2005/010550 03/29/2005  
which claims benefit of 60/557,889 03/30/2004

### Foreign Applications

**If Required, Foreign Filing License Granted:** 10/15/2008

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US 10/585,275**

**Projected Publication Date:** 01/29/2009

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Heat spreader constructions, intergrated circuitry, methods of forming heat spreader constructions, and methods of forming integrated circuitry

**Preliminary Class**

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